

# TFT DISPLAY SPECIFICATION



**WINSTAR Display Co.,Ltd.**  
**華凌光電股份有限公司**



# Winstar Display Co., LTD

## 華凌光電股份有限公司



WEB: <https://www.winstar.com.tw> E-mail: sales@winstar.com.tw

### SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:** **WF28GTLAJDNN0#**

<b>APPROVED BY:</b> ( FOR CUSTOMER USE ONLY )	<b>PCB VERSION:</b> _____ <b>DATA:</b> _____
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2018/08/06			

TFT Display Inspection Specification: <https://www.winstar.com.tw/technology/download.html>



**Winstar Display Co., LTD**  
**華凌光電股份有限公司**

MODLE NO :

**RECORDS OF REVISION**

**DOC. FIRST ISSUE**

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2018/03/16		First issue
A	2018/08/06		Add Driver IC & Uniformity

# Contents

1.Module Classification Information

2.Summary

3.General Specification

4.Absolute Maximum Ratings

5.Electrical Characteristics

6.AC Characteristics

7.Optical Characteristics

8.Interface

9.Block Diagram

10.Reliability

11.Contour Drawing

12.Other

# 1.Module Classification Information

W F 28 G T L A J D N N 0 #  
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪ ⑫ ⑬

①	Brand : WINSTAR DISPLAY CORPORATION																																																																							
②	Display Type : F→TFT Type, J→Custom TFT																																																																							
③	Display Size : 2.8” TFT																																																																							
④	Model serials no.																																																																							
⑤	Backlight Type :	F→CCFL, White S→LED, High Light White						T→LED, White Z→Nichia LED, White																																																																
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	A→Transmissive, N.T, IPS TFT C→Transmissive, N. T, 6:00 ; F→Transmissive, N.T,12:00 ; I→Transmissive, W. T, 6:00 K→Transflective, W.T,12:00 L→Transmissive, W.T,12:00 N→Transmissive, Super W.T, 6:00						Q→Transmissive, Super W.T, 12:00 R→Transmissive, Super W.T, O-TFT V→Transmissive, Super W.T, VA TFT W→Transmissive, Super W.T, IPS TFT X→Transmissive, W.T, VA TFT Y→Transmissive, W.T, IPS TFT Z→Transmissive, W.T, O-TFT																																																																
⑦	A : TFT LCD B : TFT+SCREW HOLES+CONTROL BOARD C : TFT+ SCREW HOLES +A/D BOARD D : TFT+ SCREW HOLES +A/D BOARD+CONTROL BOARD E : TFT+ SCREW HOLES +POWER BOARD						F : TFT+CONTROL BOARD G : TFT+ SCREW HOLES H : TFT+D/V BOARD I : TFT+ SCREW HOLES +D/V BOARD J : TFT+POWER BD																																																																	
⑧	Resolution: <table><tr><td>A</td><td>128160</td><td>B</td><td>320234</td><td>C</td><td>320240</td><td>D</td><td>480234</td><td>E</td><td>480272</td><td>F</td><td>640480</td></tr><tr><td>G</td><td>800480</td><td>H</td><td>1024600</td><td>I</td><td>320480</td><td>J</td><td>240320</td><td>K</td><td>800600</td><td>L</td><td>240400</td></tr><tr><td>M</td><td>1024768</td><td>N</td><td>128128</td><td>P</td><td>1280800</td><td>Q</td><td>480800</td><td>R</td><td>640320</td><td>S</td><td>480128</td></tr><tr><td>T</td><td>800320</td><td>U</td><td>8001280</td><td>V</td><td>176220</td><td>W</td><td>1280398</td><td>X</td><td>1024250</td><td>Y</td><td>1920720</td></tr><tr><td>Z</td><td>800200</td><td>2</td><td>1024324</td><td>3</td><td>7201280</td><td>4</td><td>19201200</td><td>5</td><td>1366768</td><td>6</td><td>1280320</td></tr></table>												A	128160	B	320234	C	320240	D	480234	E	480272	F	640480	G	800480	H	1024600	I	320480	J	240320	K	800600	L	240400	M	1024768	N	128128	P	1280800	Q	480800	R	640320	S	480128	T	800320	U	8001280	V	176220	W	1280398	X	1024250	Y	1920720	Z	800200	2	1024324	3	7201280	4	19201200	5	1366768	6	1280320
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⑨	D: Digital L : LVDS M:MIPI																																																																							
⑩	Interface: <table><tr><td>N</td><td>Without control board</td><td>A</td><td>8Bit</td><td>B</td><td>16Bit</td><td>H</td><td>HDMI</td></tr><tr><td>I</td><td>I2C Interface</td><td>R</td><td>RS232</td><td>S</td><td>SPI Interface</td><td>U</td><td>USB</td></tr></table>												N	Without control board	A	8Bit	B	16Bit	H	HDMI	I	I2C Interface	R	RS232	S	SPI Interface	U	USB																																												
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I	I2C Interface	R	RS232	S	SPI Interface	U	USB																																																																	
⑪	TS: <table><tr><td>N</td><td>Without TS</td><td>T</td><td>Resistive touch panel</td><td>C</td><td>Capacitive touch panel (G-F-F)</td></tr><tr><td>G</td><td>Capacitive touch panel (G-G)</td><td>C1</td><td>Capacitive touch panel (G-F-F)+OCA</td></tr><tr><td>C2</td><td>Capacitive touch panel (G-F-F)+OCR</td><td>G1</td><td>Capacitive touch panel (G-G)+OCA</td></tr><tr><td>G2</td><td>Capacitive touch panel (G-G)+OCR</td><td>B</td><td>CTP+GG+USB</td></tr></table>												N	Without TS	T	Resistive touch panel	C	Capacitive touch panel (G-F-F)	G	Capacitive touch panel (G-G)	C1	Capacitive touch panel (G-F-F)+OCA	C2	Capacitive touch panel (G-F-F)+OCR	G1	Capacitive touch panel (G-G)+OCA	G2	Capacitive touch panel (G-G)+OCR	B	CTP+GG+USB																																										
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⑫	Version: X:Raspberry pi																																																																							
⑬	Special Code	#:Fit in with ROHS directive regulations																																																																						

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WINSTAR DISPLAY Co., Ltd.

## **2.Summary**

TFT 2.8" is a TN transmissive type color active matrix TFT liquid crystal display that uses amorphous silicon TFT as switching devices. This module is composed of a TFT\_LCD module. It is usually designed for industrial application and this module follows RoHS.

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### 3.General Specifications

Item	Dimension	Unit
Size	2.8"	
Dot Matrix	240 x RGB x 320(TFT)	dots
Module dimension	50.0(W) x 69.2(H) x 2.3(D)	mm
Active area	43.2 x 57.6	mm
Dot pitch	0.18 x 0.18	mm
LCD type	TFT, Normally White, Transmissive	
TFT Interface	SPI	
TFT Driver IC	Himax8347-I	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Interface	SPI	
Aspect Ratio	Portrait	
Backlight Type	LED, Normally White	
With /Without TP	Without TP	
Surface	Glare	

\*Color tone slight changed by temperature and driving voltage.



## 4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp.  $\leq 40^{\circ}\text{C}$ , 90% RH MAX. Temp.  $> 40^{\circ}\text{C}$ , Absolute humidity shall be less than 90% RH at  $40^{\circ}\text{C}$

## 5. Electrical Characteristics

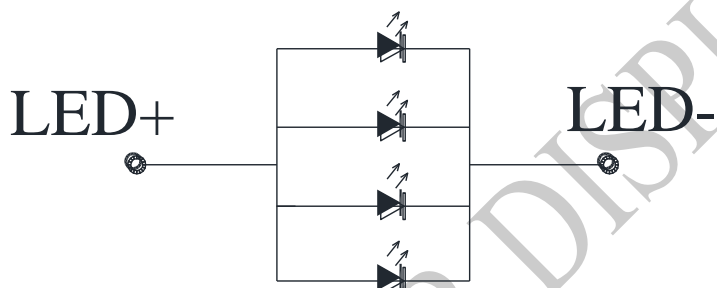
### 5.1. Operating conditions

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Analog	V <sub>DD</sub>	—	2.4	—	3.3	V
Supply Current For LCM	I <sub>DD</sub>	—	—	5	7.5	mA

### 5.2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	—	—	80	—	mA	—
Power Consumption	—	224	256	272	mW	—
LED voltage	LED+	2.8	3.2	3.4	V	Note 1
LED Life Time	—	—	30,000	—	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



### Back Light Circuit

Note 2 : Ta = 25 °C

Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

## 6.AC Characteristics

### 6.1. 4-wire Serial interface characteristics

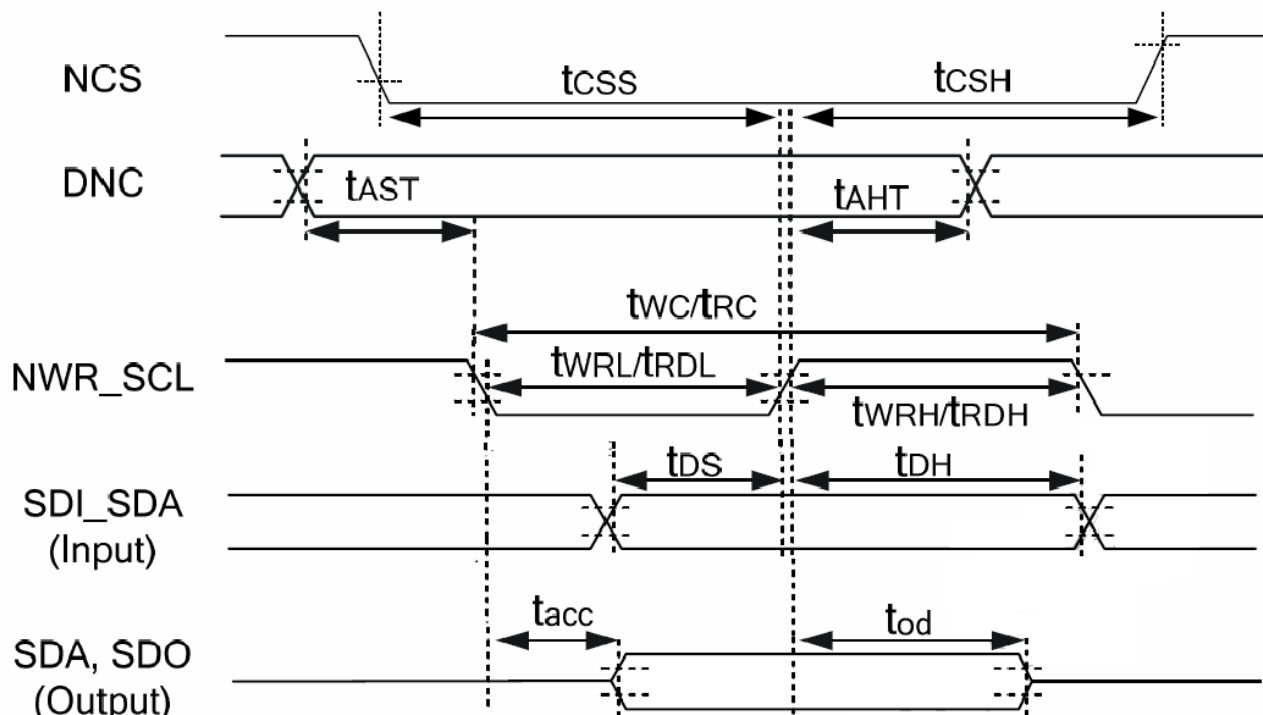


Figure 1 4-wire Serial interface characteristics

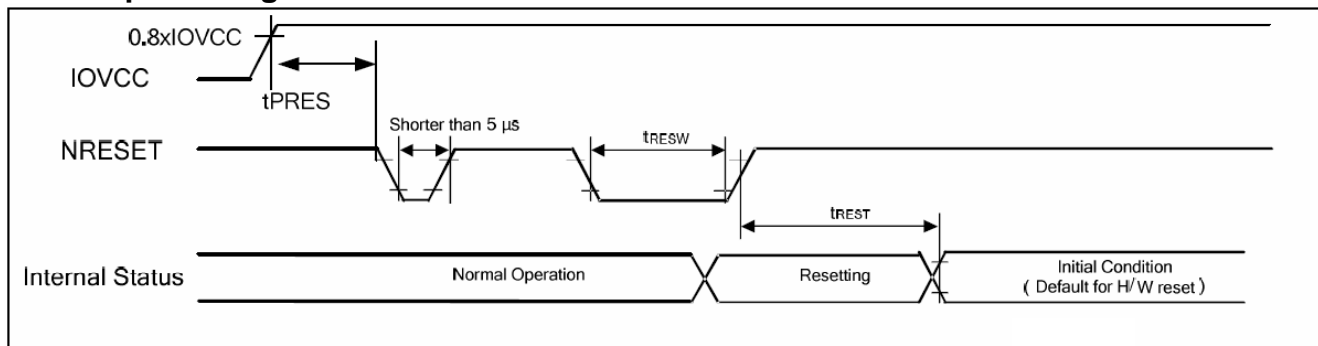
6.2. (VSSA=0V, IOVCC=1.65V to 3.3V, VCI=2.5V to 3.3V, TA=-30 to 70°C)

Signal	Symbol	Parameter	Min.	Max.	Unit	Description
NCS	$t_{CSS}$	Chip select setup time (Read)	60	-	ns	-
	$t_{CSH}$	Chip select hold time (Read)	65	-	ns	
DNC	$t_{AST}$	Address setup time	0	-	ns	-
	$t_{AHT}$	Address hold time (Write/Read)	10	-	ns	
NWR_SCL (Write)	$t_{WC}$	Write cycle	14	-	ns	-
	$t_{WRH}$	Control pulse "H" duration	6	-	ns	
	$t_{WRL}$	Control pulse "L" duration	6	-	ns	
NWR_SCL (Read)	$t_{RC}$	Read cycle	150	-	ns	-
	$t_{RDH}$	Control pulse "H" duration	60	-	ns	
	$t_{RDL}$	Control pulse "L" duration	60	-	ns	
SDI_SDA (Input)	$t_{DS}$	Data setup time	6	-	ns	For maximum CL=30pF For minimum CL=8pF
	$t_{DT}$	Data hold time	6	-	ns	
SDA, SDO (Output)	$t_{RACC}$	Read access time	10	50	ns	
	$t_{OD}$	Output disable time	15	50	ns	

Note: The input signal rise time and fall time ( $t_r$ ,  $t_f$ ) is specified at 15 ns or less.

Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

## Reset input timing

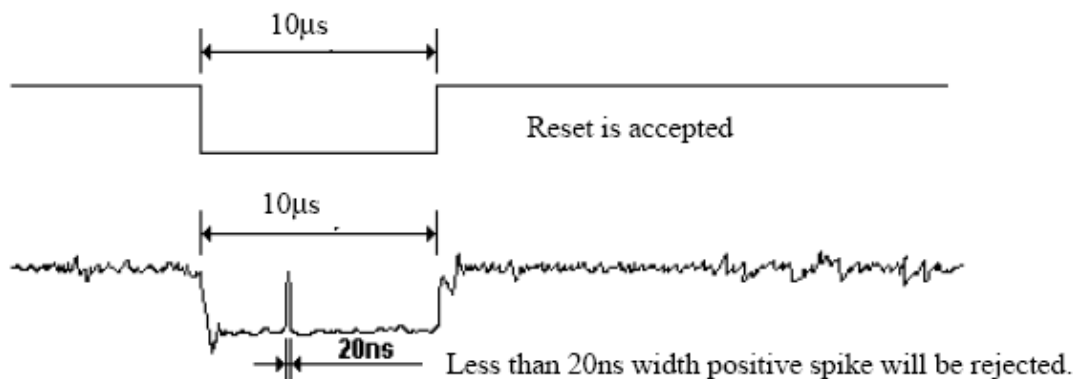


Symbol	Parameter	Related Pins	Spec.			Note	Unit
			Min.	Typ.	Max.		
tRESW	Reset low pulse width <sup>(1)</sup>	NRESET	10	-	-		us
tREST	Reset complete time <sup>(2)</sup>	-	-	-	5	When reset applied during STB OUT mode	ms
		-	-	-	120	When reset applied during STB mode	ms
tPRES	Reset goes high level after Power on time	NRESET & IOVCC	1	-	-	Reset goes high level after Power on	ms

Note: (1) Spike due to an electrostatic discharge on NRESET line does not cause irregular system reset according to the table below.

NRESET Pulse	Action
Shorter than 5 $\mu$ s	Reset Rejected
Longer than 10 $\mu$ s	Reset
Between 5 $\mu$ s and 10 $\mu$ s	Reset Start

- (2) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in STB Out –mode. The display remains the blank state in STB –mode) and then return to Default condition for H/W reset.
- (3) During Reset Complete Time, VMF value in OTP will be latched to internal register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of NRESET.
- (4) Spike Rejection also applies during a valid reset pulse as shown below:



- (5) It is necessary to wait 5msec after releasing !RES before sending commands. Also STB Out

## 7.Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr	$\theta=0^{\circ}$ 、 $\Phi=0^{\circ}$	-	4	-	ms	Note 3
		Tf		-	12	-	ms	
Contrast ratio		CR	At optimized viewing angle	-	500	-	-	Note 4
Color Chromaticity	White	Wx	$\theta=0^{\circ}$ 、 $\Phi=0$	0.26	0.31	0.36		Note 2,6,7
		Wy		0.28	0.33	0.38		
Viewing angle (Gray Scale Inversion Direction)	Hor.	$\Theta R$	$CR\geq 10$	-	45	-	Deg.	Note 1
		$\Theta L$		-	45	-		
	Ver.	$\Phi T$		-	50	-		
		$\Phi B$		-	20	-		
Brightness		-	-	400	500	-	cd/m <sup>2</sup>	Center of display
Uniformity		(U)	-	70	-	-	%	Note5

Ta=25±2℃

Note 1: Definition of viewing angle range

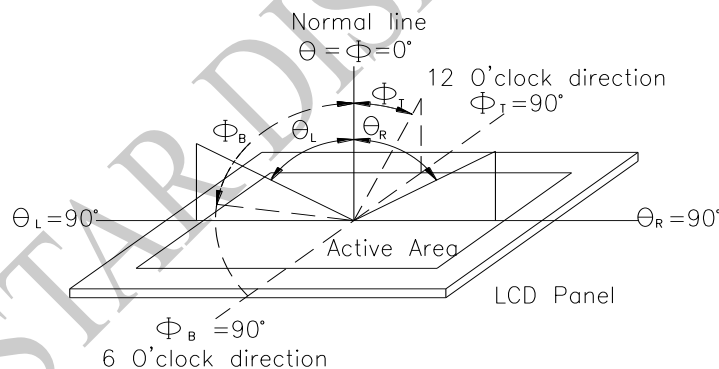


Fig. 7.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

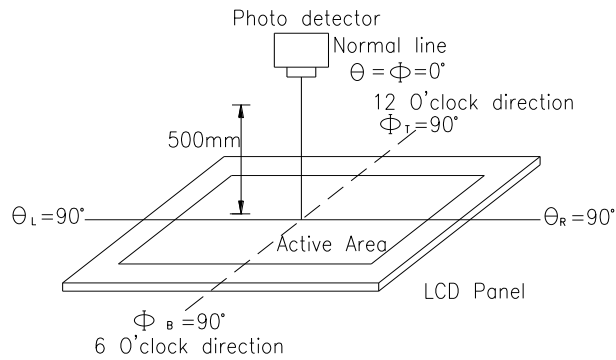
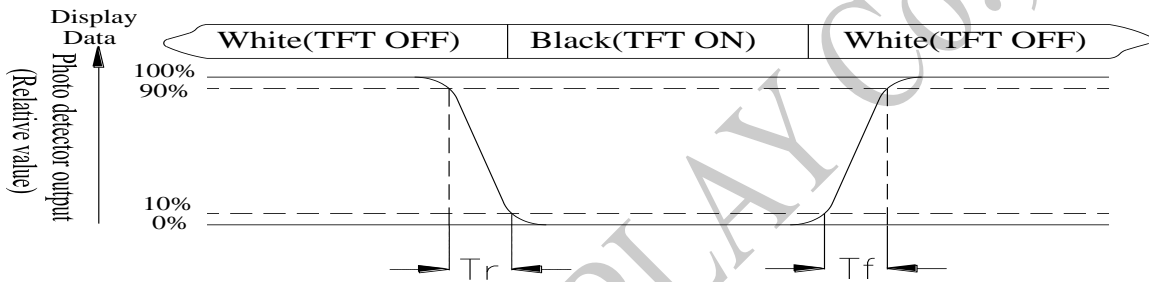


Fig. 7.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time,  $T_r$ , is the time between photo detector output intensity changed from 90% to 10%. And fall time,  $T_f$ , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = \text{Lmin/Lmax} \times 100\%$$

L = Active area length

W = Active area width

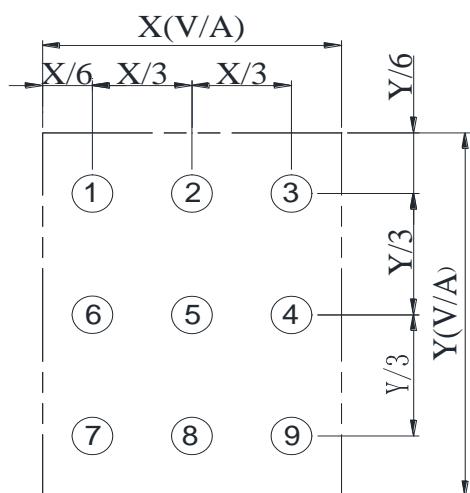


Fig7.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)  
Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

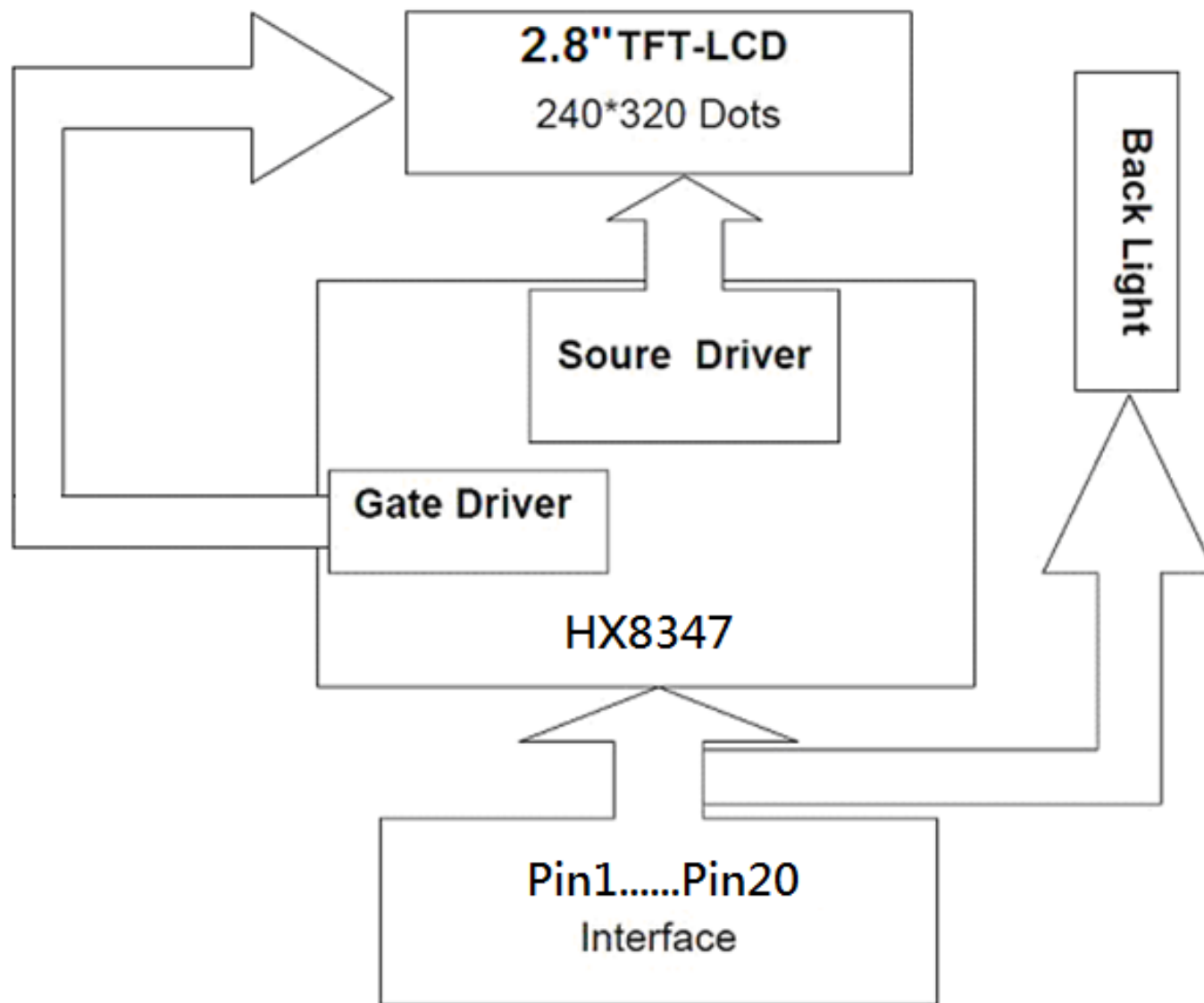
# 8.Interface

## 8.1. LCM PIN Definition

NO	Symbol	Function
1	GND	Ground
2	LED+	Anode of LED backlight.
3	LED-	Cathode of LED backlight.
4	GND	Ground
5	VDD	Power supply(TYP:2.8V).
6	RESET	System reset pin.
7	SDA	Serial data input pin and output pin(SDA) in serial bus system interface I. Serial data input pin (SDI) in serial bus system interface II. The data is inputted on the rising edge of the SCL signal. If not used, please let it open or connected to VSSD.
8	SCL	(NWR) Write enable pin I80 parallel bus system interface. (SCL) server as serial data clock in serial bus system interface when IFSEL=0. Fix it to IOVCC or VSSD level when not used.
9	CS	Chip select signal. Low: chip can be accessed; High: chip cannot be accessed. Must be connected to VSSD if not in use.
10	RS	Command / parameter or display data selection pin
11	GND	Ground
12	SDO	Serial data output pin (SDO) in serial bus system interface II. If not used, please open this pin.
13	TE	Tearing effect output. If not used, please open this pin
14	IM3	serial interface I / serial interface II
15	GND	Ground
16	NC	No connect
17	NC	No connect
18	NC	No connect
19	NC	No connect
20	GND	Ground

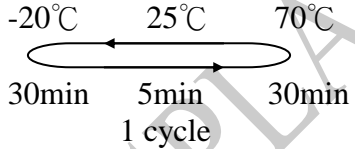


## 9. Block Diagram



# 10. Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

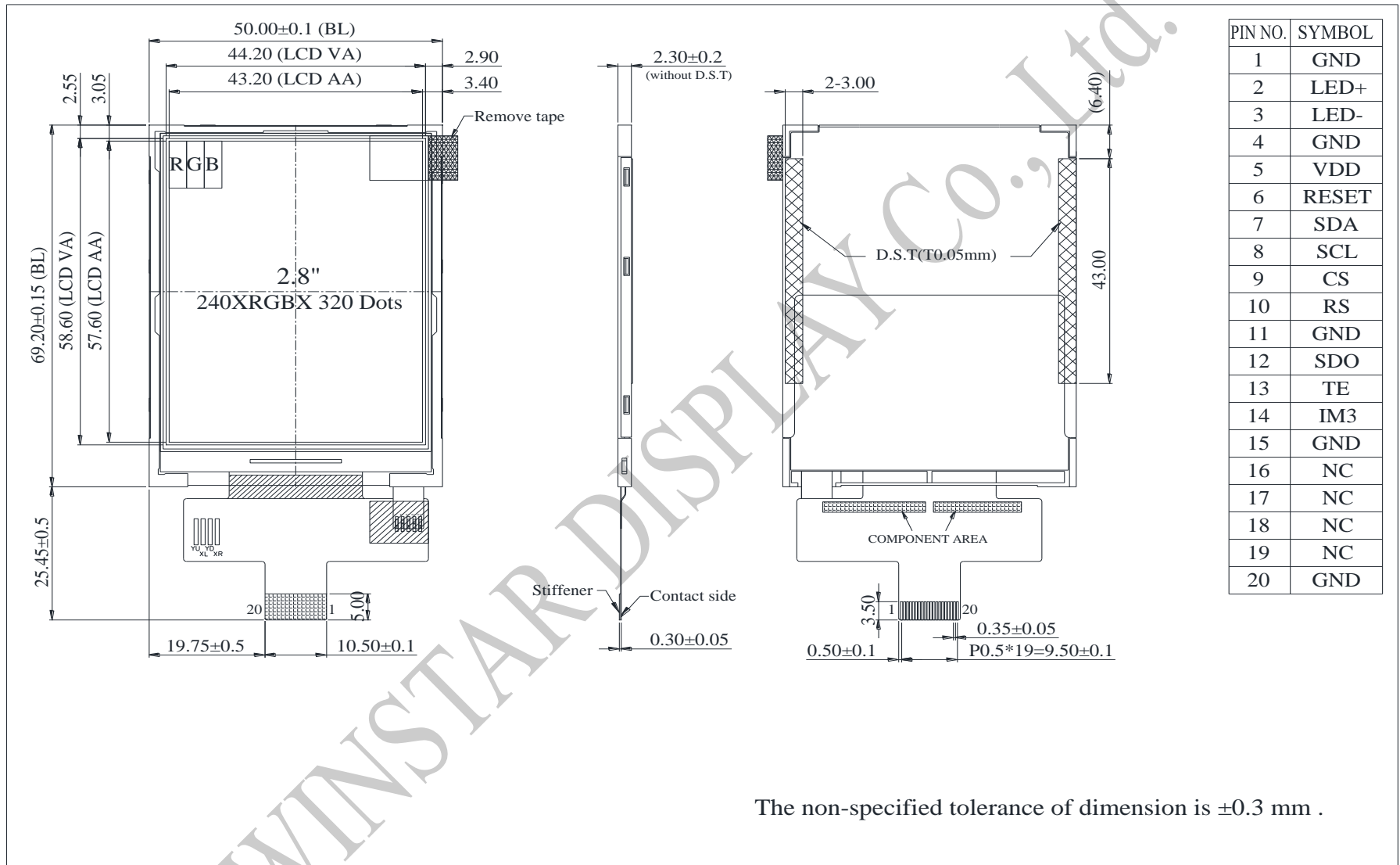
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 96hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 96hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 96hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 96hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 40 °C, 90% RH max	40°C, 90% RH 96hrs	1,2
Thermal shock resistance	<p>The sample should be allowed stand the following 10 cycles of operation</p>  <p style="text-align: center;">-20°C      25°C      70°C 30min    5min    30min 1 cycle</p>	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

# 11. Contour Drawing



**1 、 Panel Specification :**

- |                            |                               |                                     |
|----------------------------|-------------------------------|-------------------------------------|
| 1. Panel Type :            | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. View Direction :        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Numbers of Dots :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. View Area :             | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Active Area :           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Operating Temperature : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Storage Temperature :   | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. Others :                | _____                         |                                     |

**2 、 Mechanical**

- |                             |                               |                                     |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. PCB Size :               | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Frame Size :             | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Material of Frame :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Connector Position :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Fix Hole Position :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Backlight Position :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Thickness of PCB :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. Height of Frame to PCB : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. Height of Module :       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others :                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**3 、 Relative Hole Size :**

- |                             |                               |                                     |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. Pitch of Connector :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Hole size of Connector : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Mounting Hole size :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Mounting Hole Type :     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Others :                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**4 、 Backlight Specification :**

- |  |                               |                                     |
|--|-------------------------------|-------------------------------------|
| 1. B/L Type :                                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. B/L Color :                               | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. B/L Driving Voltage (Reference for LED) : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. B/L Driving Current :                     | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. Brightness of B/L :                       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. B/L Solder Method :                       | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Others :                                  | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |



Winstar      Module Number : \_\_\_\_\_

Page: 2

**5、Electronic Characteristics of Module :**

- |                              |                               |                                     |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage :           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD :        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function :      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity :          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test :                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others :                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**6、Summary :**

Sales signature : \_\_\_\_\_

Customer Signature : \_\_\_\_\_

Date :        /        /